SIEMENS

Data sheet

6ES7212-1AB22-0XB0

*** SPARE PART*** SIMATIC S7-200, CPU 222 COMPACT UNIT, DC POWER SUPPLY 8 DI DC/6 DO DC 4 KB CODE/2 KB DATA, PROFIBUS DP EXTENDABLE

	PROFIBUS DP EXTENDABLE
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
Load voltage L+	
Rated value (DC)	24 V
 permissible range, lower limit (DC) 	20.4 V
• permissible range, upper limit (DC)	28.8 V
Input current	
Inrush current, max.	10 A; at 28.8 V
from supply voltage L+, max.	500 mA; 85 mA to 500 mA, output current for expansion modules (5 V DC) 340 mA
Encoder supply	
24 V encoder supply	
• 24 V	Yes; permissible range: 15.4 to 28.8 V
 Short-circuit protection 	Yes; electronic at 600 mA
Output current, max.	180 mA
Power loss	
Power loss, typ.	5 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM
Work memory	
• into enerts of the energy	4 khyto

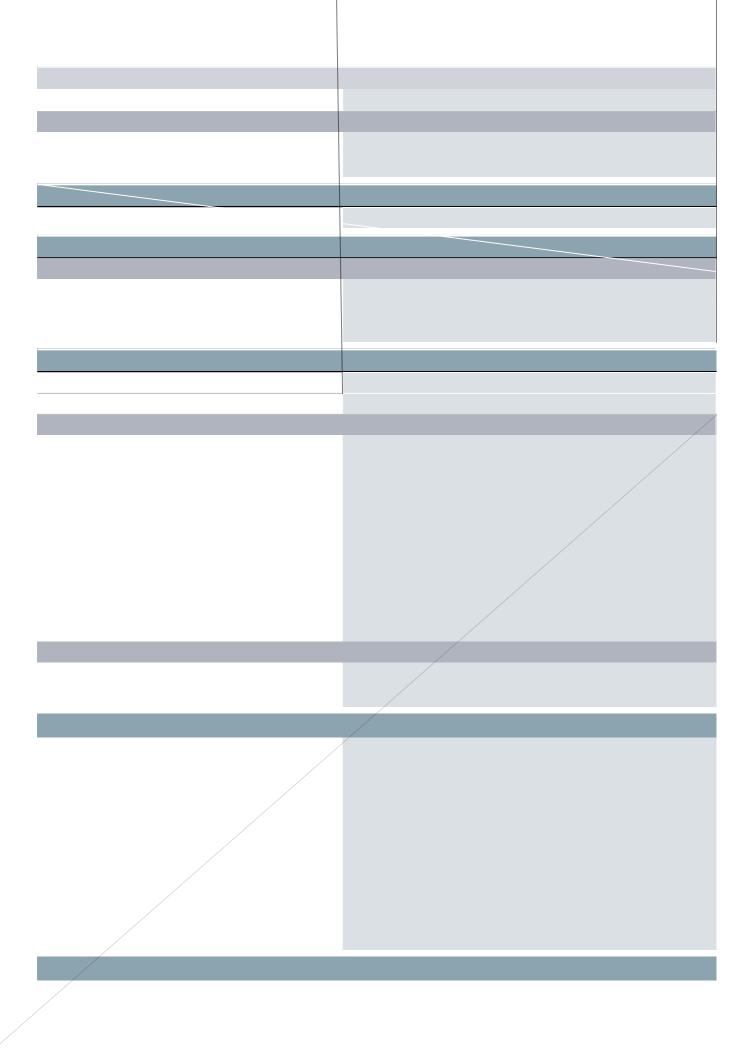
,	EEPROM
Work memory	
• integrated (for program)	4 kbyte
• integrated (for data)	2 kbyte
Backup	
• present	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering

Battery

Backup battery

Backup time, max.	50 h; (min. 8 h at 40 °C); 200 days (typ.) with optional battery module
CPU processing times	
for bit operations, max.	0.37 µs
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
S7 times	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	65
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to 54 min
Data areas and their retentivity	
Flag	
Number, max.	32 byte
 of which retentive with battery 	0 to 255, via high-performance capacitor or battery, adjustable
 of which retentive without battery 	0 to 112 in EEPROM, adjustable
Hardware configuration	
Number of expansion units, max.	2; Only expansion modules of the S7-22x series can be used. Due to the limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
Analog inputs/outputs, max.	10; max. 8 inputs and 2 outputs (EM) or max. 0 inputs and 4 outputs (EM)
 Digital inputs/outputs, max. 	78; max. 40 inputs and 38 outputs (CPU + EM)
AS-Interface inputs/outputs, max.	31; AS-Interface slaves (CP 243-2)
Digital inputs	
Number of digital inputs	8
Source/sink input	Yes; optionally, per group

Input voltage	
• Rated value (DC)	24 V
• for signal "0"	0 to 5 V
• for signal "1"	min. 15 V
Input current	
• for signal "1", typ.	4 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
	12.0 1113
for interrupt inputs	Yes; I 0.0 to I 0.3
— parameterizable	res, ro.0 to ro.3
for technological functions	V (F 0 0 to F 0 5) 20 H -
— parameterizable	Yes; (E 0.0 to E 0.5) 30 kHz
Cable length	
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
Digital outputs	
Number of digital outputs	6; Transistor
Short-circuit protection	No; to be provided externally
Limitation of inductive shutdown voltage to	1 W
Switching capacity of the outputs	
• with resistive load, max.	0.75 A
● on lamp load, max.	5 W
Output voltage	
• for signal "1", min.	20 V DC
Output current	
• for signal "1" rated value	750 mA
• for signal "0" residual current, max.	10 μΑ
Output delay with resistive load	
• "0" to "1", max.	15 μ s; of the standard outputs, max. (Q0.2 to Q0.5) 15 μ s; of the pulse outputs, max. (Q0.0 to Q0.1) 2 μ s
• "1" to "0", max.	100 μ s; of the standard outputs, max. (Q0.2 to Q0.5) 100 μ s; of the pulse outputs, max. (Q0.0 to Q0.1) 10 μ s
Parallel switching of two outputs	
• for uprating	Yes
Switching frequency	
• of the pulse outputs, with resistive load, max.	20 kHz; Q0.0 to Q0.1
Total current of the outputs (per group)	
all mounting positions	
— up to 40 °C, max.	4.5 A
•	



Potential separation digital inputs	
 between the channels 	Yes
• between the channels, in groups of	4
Potential separation digital outputs	
between the channels	Yes; Optocoupler
• between the channels, in groups of	6
Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC
Degree and class of protection	
Degree of protection acc. to EN 60529	
• IP20	Yes
Ambient conditions	
Ambient temperature during operation	
 horizontal installation, min. 	0 °C
 horizontal installation, max. 	55 °C
 vertical installation, min. 	0 °C
 vertical installation, max. 	45 °C
Air pressure acc. to IEC 60068-2-13	
permissible range, lower limit	860 hPa
• permissible range, upper limit	1 080 hPa
Relative humidity	
Operation, min.	5 %
 Operation, max. 	95 %; RH class 2 in accordance with IEC 1131-2
Configuration	
Programming	
● Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
 Program processing 	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
Program organization	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
Number of subroutines, max.	64
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
Know-how protection	
User program protection/password protection	Yes; 3-stage password protection
1 0 1	

Connection method		
Plug-in I/O terminals	No	
Dimensions		
Width	90 mm	
Height	80 mm	
Depth	62 mm	
Weights		
Weight, approx.	270 g	
last modified:	01/03/2020	