ROBOX S.P.A. - Castelletto s. Ticino (NO) Italy

RBXM MODULE

AS5024.001 - "DIG.INP." - 32-ch DIGITAL INPUT BOARD

Input description.

The first Input on the board can generate an Interrupt to the CPU.
<u>Remark:</u> the CPU can handle only one Input of this type. If more Inputs generating Interrupts are needed, the AS5024.002 board must be used.

Input / Output Words.

The Input/Output word address for each board (CPU excluded) depends on its position inside the rack. Addresses are here referred to as "BASE" + a number representing the board internal offset. To find the value of "BASE" refer to the RHW.CFG configuration file generated by the operating system during the autoconfiguration session and stored in the flash card. This file contains the address of the I/O words for each board installed in your rack. The value of "BASE" is the value of the first Input or Output word in the board.

Number of Input Words:

Numerb of Output Words: 0

"Bit 0" is the least significant bit in the word, "Bit 15" is the most significant bit in the word.

Input Word ("BASE" + 0): Inputs 1 --> 16 read out

2

0 = input in Low State; 1 = input in High State Bit 0 --> Input 1 Bit 1 --> Input 2 . Bit 15 --> Input 16

Input Word ("BASE" + 1): Inputs 17 --> 32 read out

0 = input in Low State; 1 = input in High State

Bit 0> Bit 1>	Input 17 Input 18
	•
Bit 15>	Input 32

Software Requirements.

To work correctly, the board needs the following minimum software requirements:

Operating System Flash (OSFM): Version 1.07 or higher

"RHLL" language: Version 21.04 or higher

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RIOR RACK

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Input / Output Words.

The Input/Output word address for each board (RIOB excluded) depends on its position inside the rack. Addresses are here referred to as "BASE" + a number representing the board internal offset. To find the value of "BASE" refer to the RHW.CFG configuration file generated by the operating system during the autoconfiguration session and stored in the flash card. This file contains the address of the I/O words for each board installed in your rack. The value of "BASE" is the value of the first Input or Output word in the board.

Number of Input Words:

Numerb of Output Words: 0

"Bit 0" is the least significant bit in the word, "Bit 15" is the most significant bit in the word.

Input Word ("BASE" + 0): Inputs 1 --> 16 read out

2

	0 = input	in Low State;	1 = input in High State
Bit Bit	: 0> : 1>	Input 1 Input 2	
Bit	: 15>	Input 16	

Input Word ("BASE" + 1): Inputs 17 --> 32 read out

0 = input in Low State; 1 = input in High State

Bit 0> Bit 1>	Input 17 Input 18
	•

Bit 15 --> Input 32

Software Requirements.

To work correctly, the board needs the following minimum software requirements:

Operating System Flash (OSFF): Version 1.01 or higher

Microcontroller Pic: Version 2.00 or higher

"RHLL" language: Version 21.04 or higher

Enclosure: IU5024.001 03.04.97



