## ROBOX S.P.A. - Castelletto s. Ticino (NO) Italy

## RBXM MODULE

## AS5025.001 - "DIG.OUT." - 32-channel TRANSISTOR OUTPUT BOARD


#### Abstract

Jumpers A two-position jumper is installed: RESET and HOLD. If the jumper is on RESET position, in case of system misbehaviour (WATCH DOG NOT-OK) all the outputs are set to their start condition (outputs in Low State). If the jumper is on HOLD position, or it is not installed, in case of system mishehaviour (WATCH DOG NOT-OK) all the outputs are left in their current state.


## Input / Output Words

The Input/Output word address for each board (CPU excluded) depends on its position inside the rack. Addresses are here referred to as "BASE" + a number representing the board internal offset. To find the value of "BASE" refer to the RHW.CFG configuration file generated by the operating system during the autoconfiguration session and stored in the flash card. This file contains the address of the I/O words for each board installed in your rack. The value of "BASE" is the value of the first Input or Output word in the board.

Number of Input Words: 1
Numerb of Output Words: 2
"Bit 0 " is the least significant bit in the word, "Bit 15 " is the most significant bit in the word.

| Input Word ("BASE" + 0): | State of supplies and output transistors |
| :---: | :---: |
| Bit 0: | 1 = transistor alarm for outputs 1.8 |
| Bit 1: | 1 = transistor alarm for outputs $9 . .16$ |
| Bit 2: | 1 = transistor alarm for outputs 17..24 |
| Bit 3: | 1 = transistor alarm for outputs $25 . .32$ |
| Bit 4: | 1 = supply alarm for outputs $1 . .8$ |
| Bit 5: | 1 = supply alarm for outputs $9 . .16$ |
| Bit 6: | 1 = supply alarm for outputs $17 . .24$ |
| Bit 7: | 1 = supply alarm for outputs $25 . .32$ |
| Bit 8 --> 15 | non significant |
| Output Word ("BASE" + 0): Output transistors 1 --> 16 |  |
| 0 = Output in Low State (Off); 1 = Output in High State (On) |  |
|  | Bit $0-->$ Output 1 |
|  | Bit $1-->$ Output 2 |
|  | . |
|  | Bit 15 --> Output 16 |
| Output Word ("BASE" + 1): Output transistors 17 --> 32 |  |
| $0=$ Output in Low State (Off); 1 = Output in High State (On) |  |
|  | Bit $0-->$ Output 17 |
|  | Bit $1-->$ Output 18 |
|  | - Outhe 18 |
|  | Bit 15 --> Output 32 |

At the system reset all the outputs are set to Low State (Off).
Software Requirements.
To work correctly, the board needs the following minimum software requirements:
Operating System Flash (OSFM): Version 1.07 or higher
"RHLL" language: Version 21.04 or higher

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## RIOR RACK

## AS5025.001 - "DIG.OUT." - 32-channel TRANSISTOR OUTPUT BOARD <br> Jumpers

A two-position jumper is installed: RESET and HOLD.
If the jumper is on RESET position, in case of system misbehaviour (WATCH DOG NOT-OK) all the outputs are set to their start condition (outputs il Low State).
If the jumper is on HOLD position, or it is not installed, in case of system mishehaviour (WATCH DOG NOT-OK) all the outputs are left in their current state.

## Input / Output Words.

The Input/Output word address for each board (RIOB excluded) depends on its position inside the rack. Addresses are here referred to as "BASE" + a number representing the board internal offset. To find the value of "BASE" refer to the RHW.CFG configuration file generated by the operating system during the autoconfiguration session and stored in the flash card. This file contains the address of the I/O words for each board installed in your rack. The value of "BASE" is the value of the first Input or Output word in the board.

Number of Input Words:
1
Numerb of Output Words: 2
Number of State words: 1 (it is treated as an output word)
"Bit 0 " is the least significant bit in the word, "Bit 15" is the most significant bit in the word.
State Word: $\quad$ State of output supplies and transistors
It is coded as an Output Word; its address is: $200+($ "W,S, Number " * 50$)+$ "Number of Slot"
Bit 0: $\quad 1=$ transistor alarm for outputs $1 . .8$
Bit 1: $\quad 1=$ transistor alarm for outputs $9 . .16$
Bit 2: $\quad 1=$ transistor alarm for outputs $17 . .24$
Bit 3: $\quad 1=$ transistor alarm for outputs 25.. 32
Bit 4: $\quad 1=$ supply alarm for outputs $1 . .8$
Bit 5: $\quad 1=$ supply alarm for outputs $9 . .16$
Bit 6: $\quad 1=$ supply alarm for outputs $17 . .24$
Bit 7: $\quad 1=$ supply alarm for outputs $25 . .32$
Bit 8 --> 15 non significant
Output Word ("BASE" + 0): Output Transistors 1 --> 16
$0=$ Output in Low State (Off); $1=$ Output in High State (On)
Bit 0 --> Output 1
Bit 1 --> Output 2

Bit 15 --> Output 16

Output Word ("BASE" + 1): Output Transistors 17 --> 32
$0=$ Output in Low State (Off); $1=$ Output in High State (On)
Bit 0 --> Output 17
Bit 1 --> Output 18

Bit 15 --> Output 32
At the system reset all the outputs are set to Low State (Off).
Software Requirements.
To work correctly, the board needs the following minimum software requirements:
Operating System Flash (OSFF): Version 1.01 or higher
Microcontroller Pic: Version 2.00 or higher
"RHLL" language: Version 21.04 or higher

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